

SIMATIC S7-300, CPU 312C Compact CPU with MPI, 10 DI/6 DQ, 2 high-speed counters (10 kHz) Integr. power supply 24 V DC, work memory 64 KB, Front connector (1x 40-pole) and Micro Memory Card required



General information	
HW functional status	01
Firmware version	V3.3
Engineering with	
<ul style="list-style-type: none"> <li>Programming package</li> </ul>	STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
Supply voltage	
Rated value (DC)	
<ul style="list-style-type: none"> <li>24 V DC</li> </ul>	Yes
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
Mains buffering	
<ul style="list-style-type: none"> <li>Mains/voltage failure stored energy time</li> <li>Repeat rate, min.</li> </ul>	5 ms 1 s
Load voltage L+	
Digital outputs	
— Rated value (DC)	24 V

— Reverse polarity protection

No

### Input current

Current consumption (rated value)	570 mA
Current consumption (in no-load operation), typ.	90 mA
Inrush current, typ.	5 A
$I^2t$	0.7 A <sup>2</sup> ·s

### Digital outputs

<ul style="list-style-type: none"> <li>from load voltage L+, max.</li> </ul>	25 mA
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### Power loss

Power loss, typ.	8 W
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### Memory

<b>Work memory</b>	
<ul style="list-style-type: none"> <li>integrated</li> </ul>	64 kbyte
<ul style="list-style-type: none"> <li>expandable</li> </ul>	No
<ul style="list-style-type: none"> <li>Size of retentive memory for retentive data blocks</li> </ul>	64 kbyte
<b>Load memory</b>	
<ul style="list-style-type: none"> <li>Plug-in (MMC)</li> </ul>	Yes
<ul style="list-style-type: none"> <li>Plug-in (MMC), max.</li> </ul>	8 Mbyte
<ul style="list-style-type: none"> <li>Data management on MMC (after last programming), min.</li> </ul>	10 y
<b>Backup</b>	
<ul style="list-style-type: none"> <li>present</li> </ul>	Yes; Guaranteed by MMC (maintenance-free)
<ul style="list-style-type: none"> <li>without battery</li> </ul>	Yes; Program and data

### CPU processing times

for bit operations, typ.	0.1 μs
for word operations, typ.	0.24 μs
for fixed point arithmetic, typ.	0.32 μs
for floating point arithmetic, typ.	1.1 μs

### CPU-blocks

Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
<b>DB</b>	
<ul style="list-style-type: none"> <li>Number, max.</li> </ul>	1 024; Number range: 1 to 16000
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	64 kbyte
<b>FB</b>	
<ul style="list-style-type: none"> <li>Number, max.</li> </ul>	1 024; Number range: 0 to 7999
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	64 kbyte
<b>FC</b>	
<ul style="list-style-type: none"> <li>Number, max.</li> </ul>	1 024; Number range: 0 to 7999
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	64 kbyte

OB	
• Description	see instruction list
• Size, max.	64 kbyte
• Number of free cycle OBs	1; OB 1
• Number of time alarm OBs	1; OB 10
• Number of delay alarm OBs	2; OB 20, 21
• Number of cyclic interrupt OBs	4; OB 32, 33, 34, 35
• Number of process alarm OBs	1; OB 40
• Number of startup OBs	1; OB 100
• Number of asynchronous error OBs	4; OB 80, 82, 85, 87
• Number of synchronous error OBs	2; OB 121, 122
Nesting depth	
• per priority class	16
• additional within an error OB	4
Counters, timers and their retentivity	
S7 counter	
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	Z 0 to Z 7
Counting range	
— lower limit	0
— upper limit	999
IEC counter	
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)
S7 times	
• Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	No retentivity
Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes

• Type	SFB
• Number	Unlimited (limited only by RAM capacity)

### Data areas and their retentivity

retentive data area in total	All, max. 64 KB
<b>Flag</b>	
• Number, max.	256 byte
• Retentivity preset	MB 0 to MB 15
• Number of clock memories	8; 1 memory byte
<b>Data blocks</b>	
• Retentivity adjustable	Yes; via non-retain property on DB
• Retentivity preset	Yes

### Address area

<b>I/O address area</b>	
• Inputs	1 024 byte
• Outputs	1 024 byte
<b>Process image</b>	
• Inputs	1 024 byte
• Outputs	1 024 byte
• Inputs, adjustable	1 024 byte
• Outputs, adjustable	1 024 byte
• Inputs, default	128 byte
• Outputs, default	128 byte
<b>Default addresses of the integrated channels</b>	
— Digital inputs	124.0 to 125.1
— Digital outputs	124.0 to 124.5
<b>Digital channels</b>	
• Inputs	266
— of which central	266
• Outputs	262
— of which central	262
<b>Analog channels</b>	
• Inputs	64
— of which central	64
• Outputs	64
— of which central	64
<b>Hardware configuration</b>	
Number of expansion units, max.	0
<b>Number of DP masters</b>	
• integrated	none
• via CP	4

Number of operable FMs and CPs (recommended)	
• FM	8
• CP, PtP	8
• CP, LAN	4
Rack	
• Racks, max.	1
• Modules per rack, max.	8
Time of day	
Clock	
• Software clock	Yes
• retentive and synchronizable	No; Buffered: No, Can be synchronized: Yes
• Deviation per day, max.	10 s; Typ.: 2 s
• Behavior of the clock following POWER-ON	The clock continues at the time of day it had when power was switched off
Operating hours counter	
• Number	1
• Number/Number range	0
• Range of values	0 to 2 <sup>31</sup> hours (when using SFC 101)
• Granularity	1 h
• retentive	Yes; Must be restarted at each restart
Clock synchronization	
• supported	Yes
• to MPI, master	Yes
• to MPI, slave	Yes
• in AS, master	Yes
• in AS, slave	No
Digital inputs	
Number of digital inputs	10
• of which inputs usable for technological functions	8
integrated channels (DI)	10
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	10
— up to 60 °C, max.	5
vertical installation	
— up to 40 °C, max.	5
Input voltage	
• Rated value (DC)	24 V

• for signal "0"	-3 to +5V
• for signal "1"	+15 to +30V
<b>Input current</b>	
• for signal "1", typ.	8 mA
<b>Input delay (for rated value of input voltage)</b>	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for technological functions	
— at "0" to "1", max.	48 $\mu$ s; Minimum pulse width/minimum pause between pulses at maximum counting frequency
<b>Cable length</b>	
• shielded, max.	1 000 m; 100 m for technological functions
• unshielded, max.	600 m; for technological functions: No
for technological functions	
— shielded, max.	100 m; at maximum count frequency
— unshielded, max.	not allowed
<b>Digital outputs</b>	
Number of digital outputs	6
• of which high-speed outputs	2; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	6
Short-circuit protection	Yes; Clocked electronically
• Response threshold, typ.	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Controlling a digital input	Yes
<b>Switching capacity of the outputs</b>	
• on lamp load, max.	5 W
<b>Load resistance range</b>	
• lower limit	48 $\Omega$
• upper limit	4 k $\Omega$
<b>Output voltage</b>	
• for signal "1", min.	L+ (-0.8 V)
<b>Output current</b>	
• for signal "1" rated value	500 mA
• for signal "1" permissible range, min.	5 mA
• for signal "1" permissible range, max.	0.6 A
• for signal "1" minimum load current	5 mA
• for signal "0" residual current, max.	0.5 mA
<b>Parallel switching of two outputs</b>	

• for uprating	No
• for redundant control of a load	Yes
<b>Switching frequency</b>	
• with resistive load, max.	100 Hz
• with inductive load, max.	0.5 Hz
• on lamp load, max.	100 Hz
• of the pulse outputs, with resistive load, max.	2.5 kHz
<b>Total current of the outputs (per group)</b>	
horizontal installation	
— up to 40 °C, max.	2 A
— up to 60 °C, max.	1.5 A
vertical installation	
— up to 40 °C, max.	1.5 A
<b>Cable length</b>	
• shielded, max.	1 000 m
• unshielded, max.	600 m
<b>Analog inputs</b>	
Number of analog inputs	0
integrated channels (AI)	0
<b>Analog outputs</b>	
Number of analog outputs	0
integrated channels (AO)	0
<b>Encoder</b>	
Connectable encoders	
• 2-wire sensor	Yes
— permissible quiescent current (2-wire sensor), max.	1.5 mA
<b>Interfaces</b>	
Number of industrial Ethernet interfaces	0
Number of PROFINET interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	0
<b>1. Interface</b>	
Interface type	Integrated RS 485 interface
Physics	RS 485
Isolated	No
Power supply to interface (15 to 30 V DC), max.	200 mA
<b>Protocols</b>	
• MPI	Yes
• PROFIBUS DP master	No

• PROFIBUS DP slave	No
• Point-to-point connection	No
<b>MPI</b>	
• Transmission rate, max.	187.5 kbit/s
<b>Services</b>	
— PG/OP communication	Yes
— Routing	No
— Global data communication	Yes
— S7 basic communication	Yes
— S7 communication	Yes; Only server, configured on one side
— S7 communication, as client	No; but via CP and loadable FB
— S7 communication, as server	Yes
<b>Communication functions</b>	
PG/OP communication	Yes
Data record routing	No
<b>Global data communication</b>	
• supported	Yes
• Number of GD loops, max.	8
• Number of GD packets, max.	8
• Number of GD packets, transmitter, max.	8
• Number of GD packets, receiver, max.	8
• Size of GD packets, max.	22 byte
• Size of GD packet (of which consistent), max.	22 byte
<b>S7 basic communication</b>	
• supported	Yes
• User data per job, max.	76 byte
• User data per job (of which consistent), max.	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)
<b>S7 communication</b>	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
• User data per job, max.	180 byte; (with PUT/GET)
• User data per job (of which consistent), max.	240 byte; as server
<b>S5 compatible communication</b>	
• supported	Yes; via CP and loadable FC
<b>Number of connections</b>	
• overall	6
• usable for PG communication	5
— reserved for PG communication	1
— adjustable for PG communication, min.	1



- adjustable for PG communication, max. 5
- usable for OP communication 5
  - reserved for OP communication 1
  - adjustable for OP communication, min. 1
  - adjustable for OP communication, max. 5
- usable for S7 basic communication 2
  - reserved for S7 basic communication 0
  - adjustable for S7 basic communication, min. 0
  - adjustable for S7 basic communication, max. 2

### S7 message functions

Number of login stations for message functions, max.	6; Depending on the configured connections for PG/OP and S7 basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	300

### Test commissioning functions

Status block	Yes; Up to 2 simultaneously
Single step	Yes
Number of breakpoints	4

<b>Status/control</b>	
• Status/control variable	Yes
• Variables	Inputs, outputs, memory bits, DB, times, counters
• Number of variables, max.	30
— of which status variables, max.	30
— of which control variables, max.	14

<b>Forcing</b>	
• Forcing	Yes
• Forcing, variables	Inputs, outputs
• Number of variables, max.	10

<b>Diagnostic buffer</b>	
• present	Yes
• Number of entries, max.	500
— adjustable	No
— of which powerfail-proof	100; Only the last 100 entries are retained
• Number of entries readable in RUN, max.	499
— adjustable	Yes; From 10 to 499
— preset	10

<b>Service data</b>	
• can be read out	Yes

### Interrupts/diagnostics/status information

Diagnostics indication LED	
• Status indicator digital input (green)	Yes
• Status indicator digital output (green)	Yes

Integrated Functions	
Number of counters	2; See "Technological Functions" manual
Counting frequency (counter) max.	10 kHz
Frequency measurement	Yes
Number of frequency meters	2; up to 10 kHz (see "Technological Functions" manual)
controlled positioning	No
integrated function blocks (closed-loop control)	No
PID controller	No
Number of pulse outputs	2; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)
Limit frequency (pulse)	2.5 kHz

Potential separation	
Potential separation digital inputs	
• Potential separation digital inputs	Yes
• between the channels	No
• between the channels and backplane bus	Yes
Potential separation digital outputs	
• Potential separation digital outputs	Yes
• between the channels	No
• between the channels and backplane bus	Yes

Isolation	
Isolation tested with	600 V DC

Ambient conditions	
Ambient temperature during operation	
• min.	0 °C
• max.	60 °C

Configuration	
Configuration software	
• STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
• STEP 7 Lite	No
Programming	
• Command set	see instruction list
• Nesting levels	8
• System functions (SFC)	see instruction list
• System function blocks (SFB)	see instruction list
Programming language	
— LAD	Yes

- FBD
- STL
- SCL
- GRAPH
- HiGraph®

Yes  
Yes  
Yes  
Yes  
Yes

#### Know-how protection

- User program protection/password protection
- Block encryption

Yes  
Yes; With S7 block Privacy

#### Dimensions

Width	80 mm
Height	125 mm
Depth	130 mm

#### Weights

Weight, approx.	410 g
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**last modified:** 11/16/2019